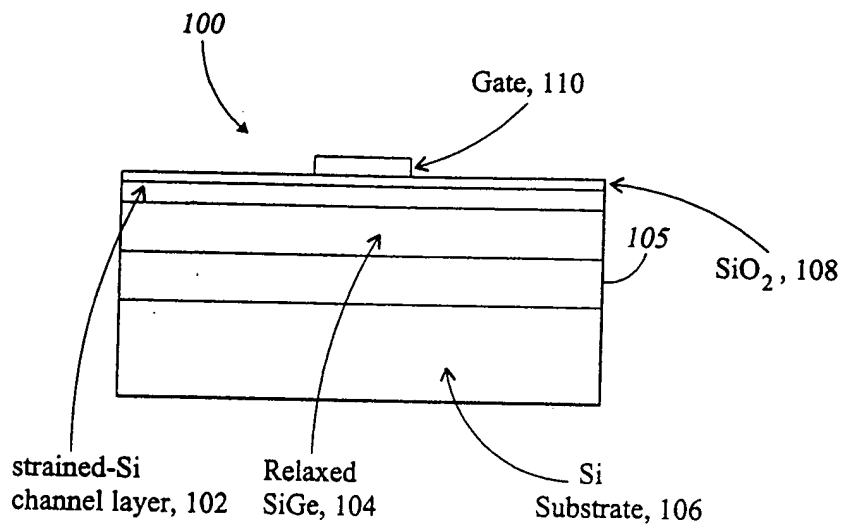




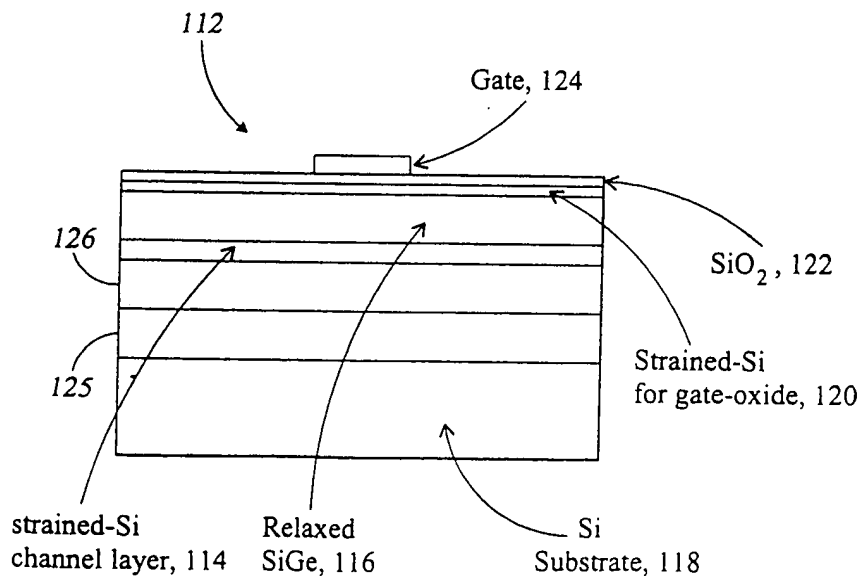
"Gate Technology for Strained Surface Channel and  
Strained Buried Channel MOSFET Devices"  
Inventors: Fitzgerald et al.  
Serial No.: 09/923,207 Filed: August 6, 2001  
Attorney Docket No.: ASC-042  
Sheet 1 of 12  
Express Mail Mailing Label No. EV289511737US

1 / 12

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**FIG. 1A**



**FIG. 1B**



"Gate Technology for Strained Surface Channel and Strained Buried Channel MOSFET Devices"

Inventors: Fitzgerald et al.

Serial No.: 09/923,207

Filed: August 6, 2001

Attorney Docket No.: ASC-042

Sheet 2 of 12

Express Mail Mailing Label No. EV289511737US

2/12

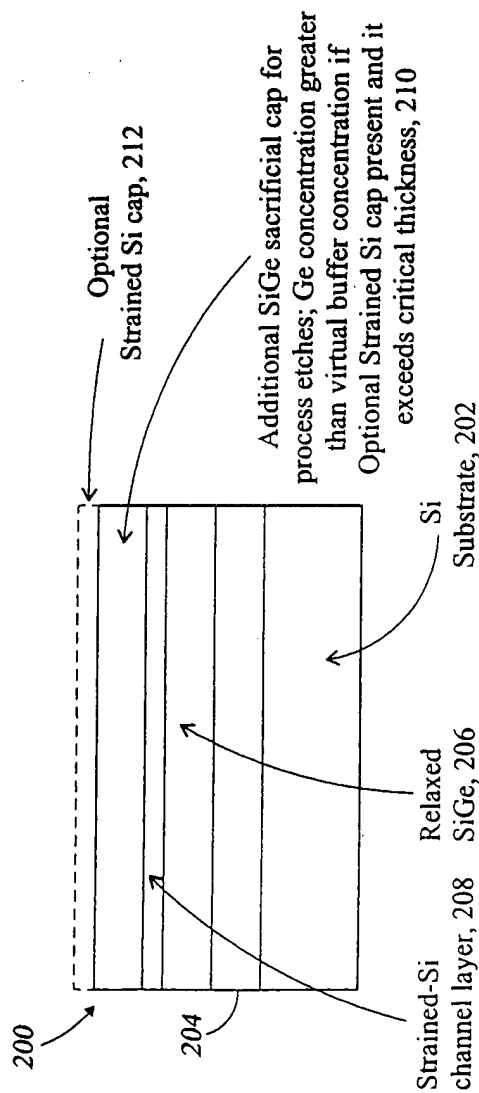


FIG. 2A



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Serial No.: 09/923,207 Filed: August 6, 2001  
Attorney Docket No.: ASC-042  
Sheet 3 of 12  
Express Mail Mailing Label No. EV289511737US

3/12

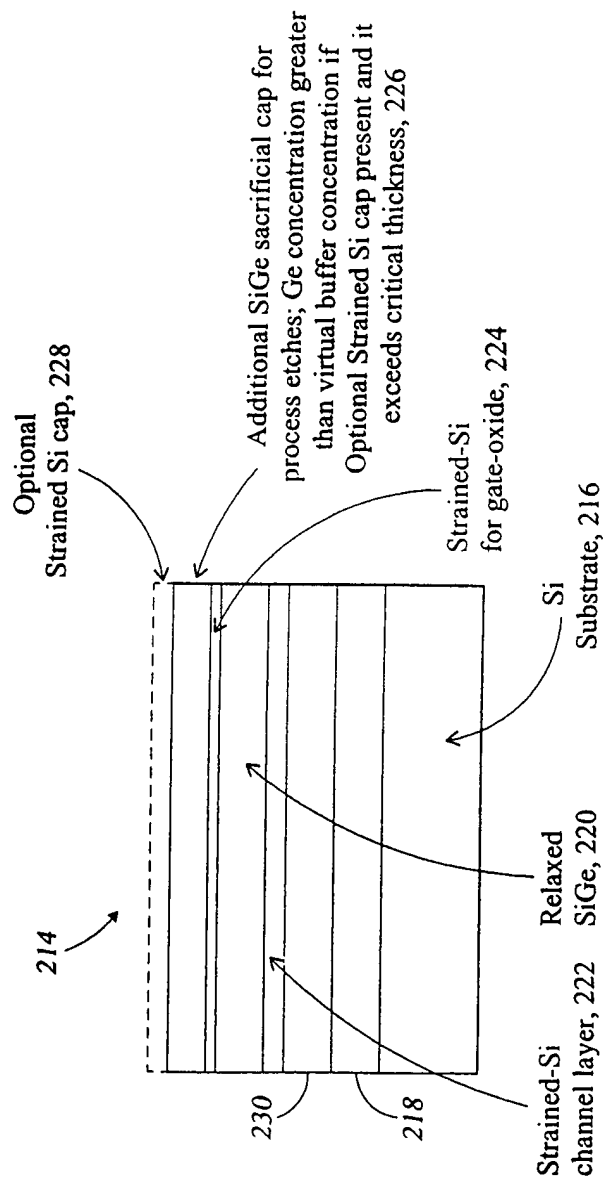
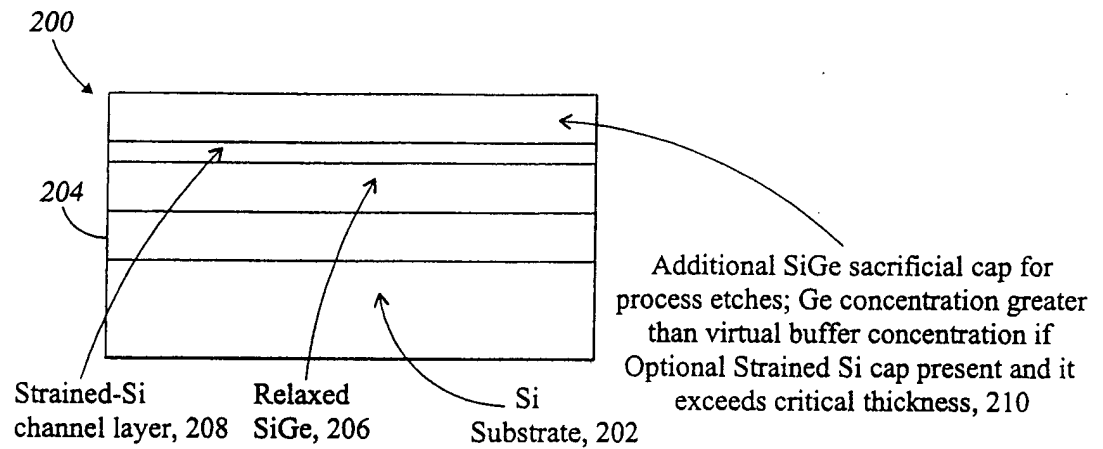


FIG. 2B

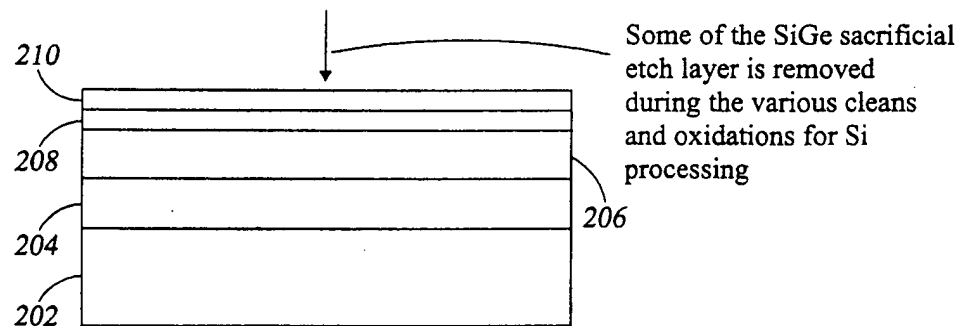


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Serial No.: 09/923,207 Filed: August 6, 2001  
Attorney Docket No.: ASC-042  
Sheet 4 of 12  
Express Mail Mailing Label No. EV289511737US

4 / 12



*FIG. 3A*



*FIG. 3B*



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Inventors: Fitzgerald et al.

Serial No.: 09/923,207

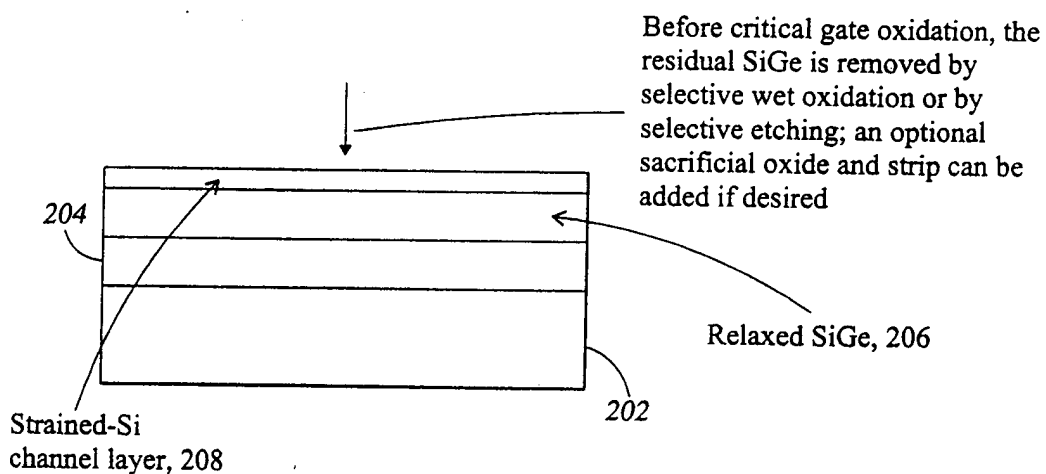
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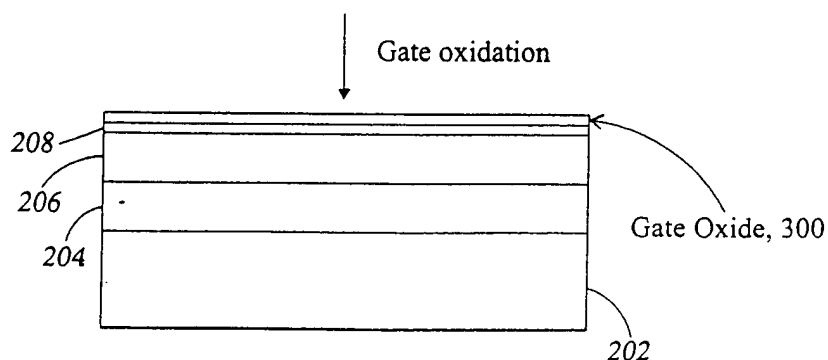
Sheet 5 of 12

Express Mail Mailing Label No. EV289511737US

5/12



*FIG. 3C*



*FIG. 3D*



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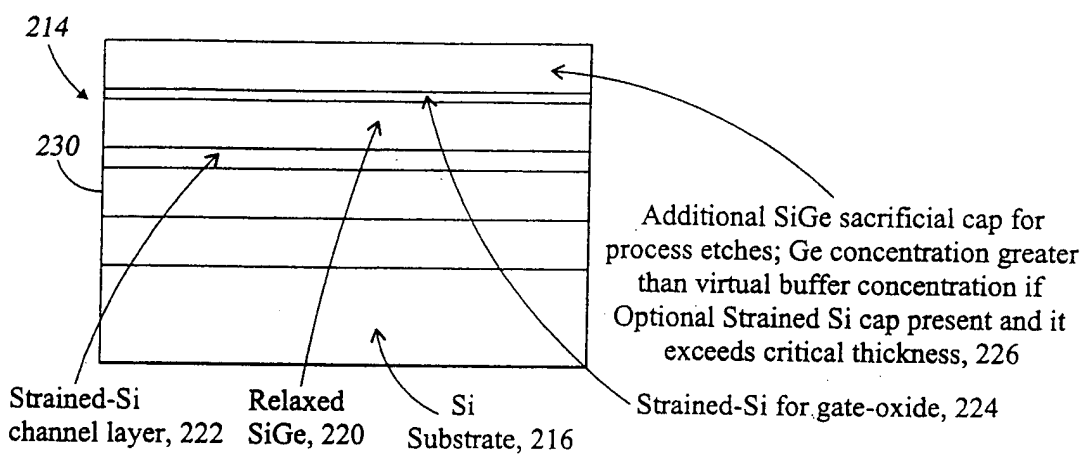
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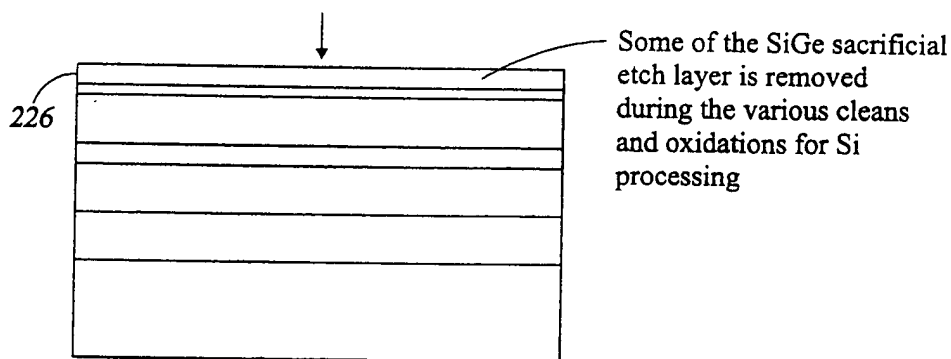
Sheet 6 of 12

Express Mail Mailing Label No. EV289511737US

6 / 12



**FIG. 4A**

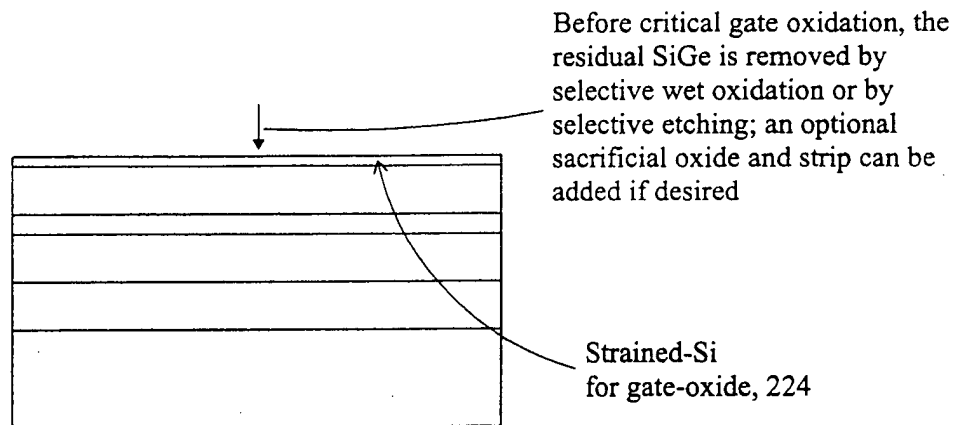


**FIG. 4B**

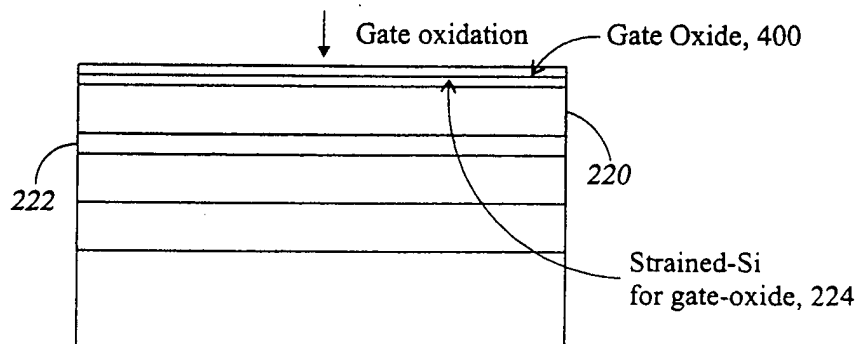


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Serial No.: 09/923,207 Filed: August 6, 2001  
Attorney Docket No.: ASC-042  
Sheet 7 of 12  
Express Mail Mailing Label No. EV289511737US

7/12



*FIG. 4C*



*FIG. 4D*



"Gate Technology for Strained Surface Channel and Strained Buried Channel MOSFET Devices"  
 Inventors: Fitzgerald et al.  
 Serial No.: 09/923,207 Filed: August 6, 2001  
 Attorney Docket No.: ASC-042  
 Sheet 8 of 12  
 Express Mail Mailing Label No. EV289511737US

8/12

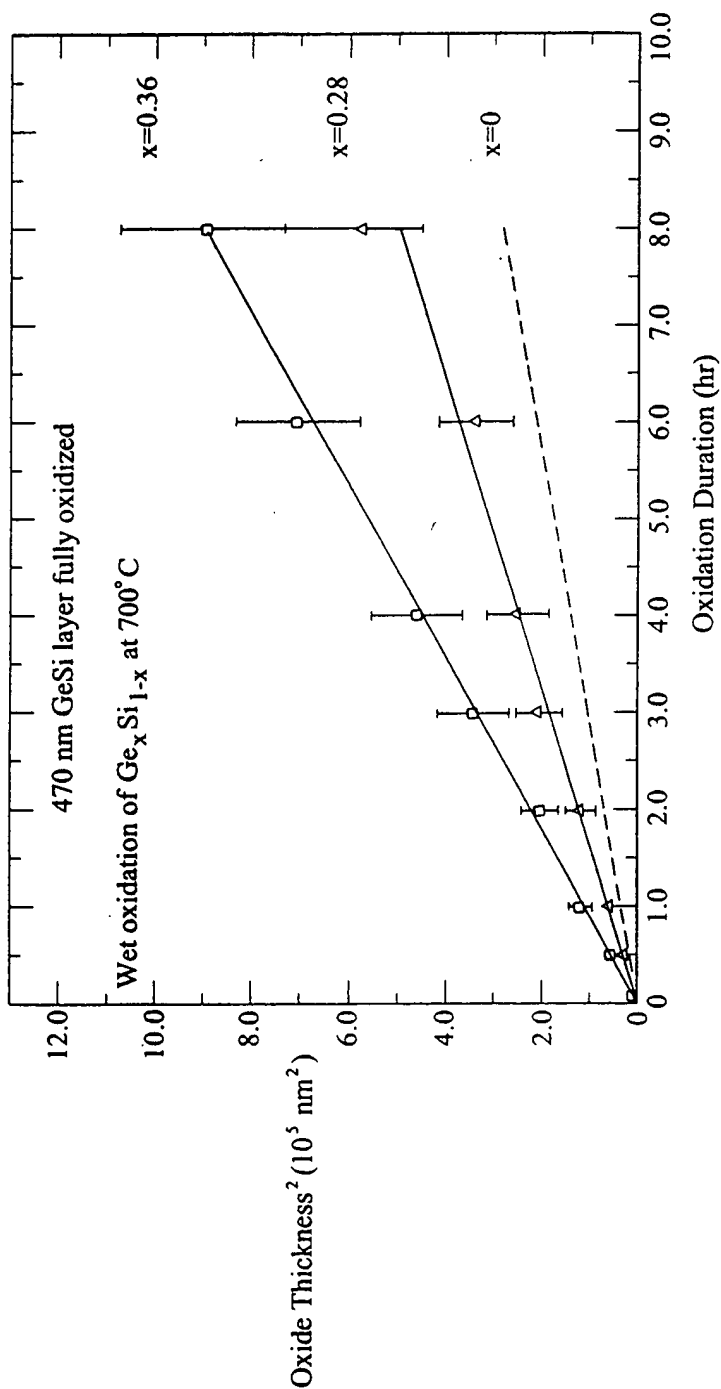


FIG. 5





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Serial No.: 09/923,207 Filed: August 6, 2001  
Attorney Docket No.: ASC-042  
Sheet 9 of 12  
Express Mail Mailing Label No. EV289511737US

9/12

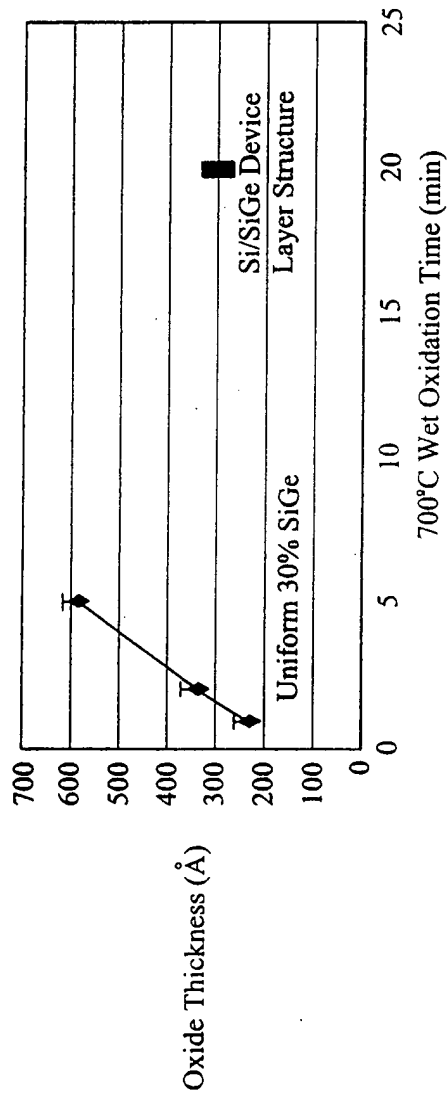
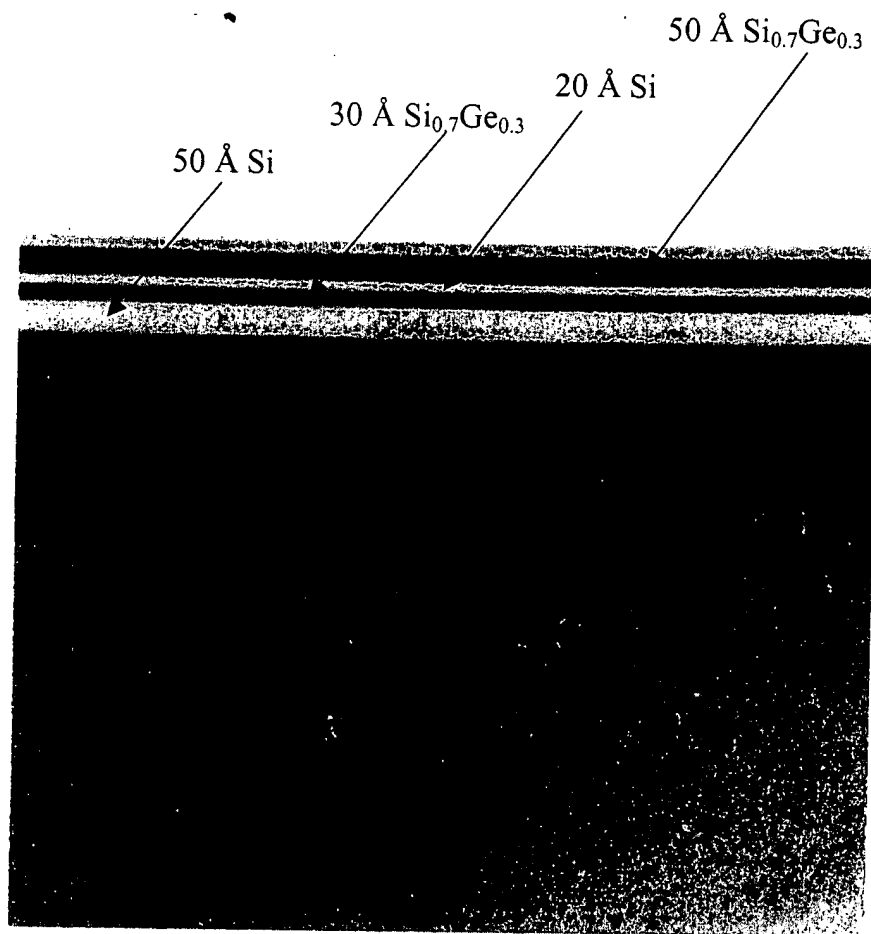


FIG. 6

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Attorney Docket No.: ASC-042  
Sheet 10 of 12  
Express Mail Mailing Label No. EV289511737US

10/12

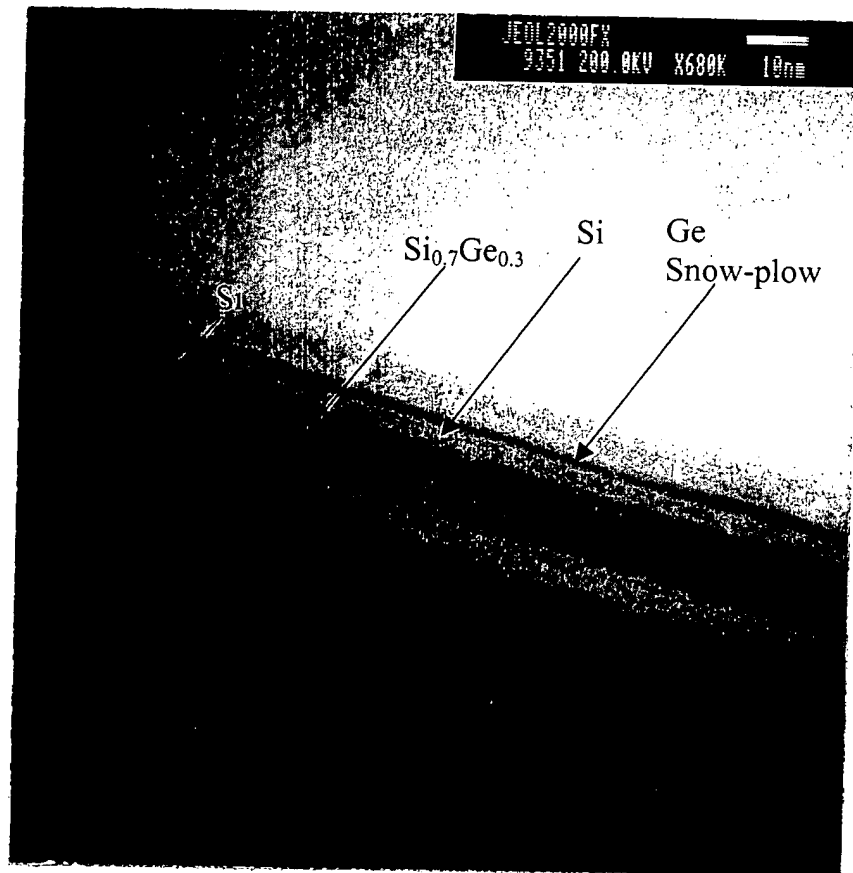


*FIG. 7*

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Attorney Docket No.: ASC-042  
Sheet 11 of 12  
Express Mail Mailing Label No. EV289511737US

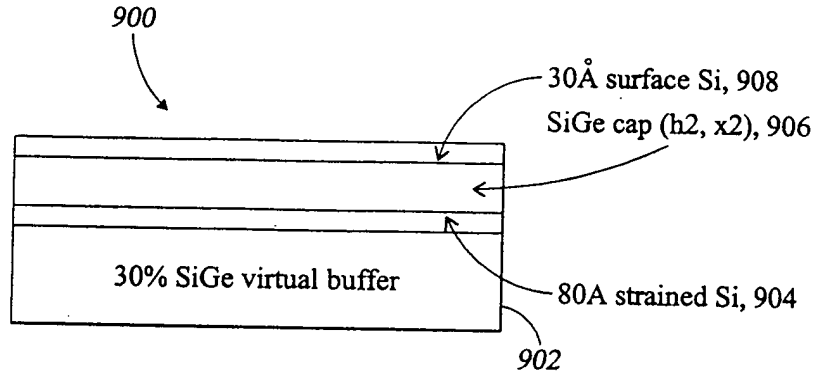
11/12



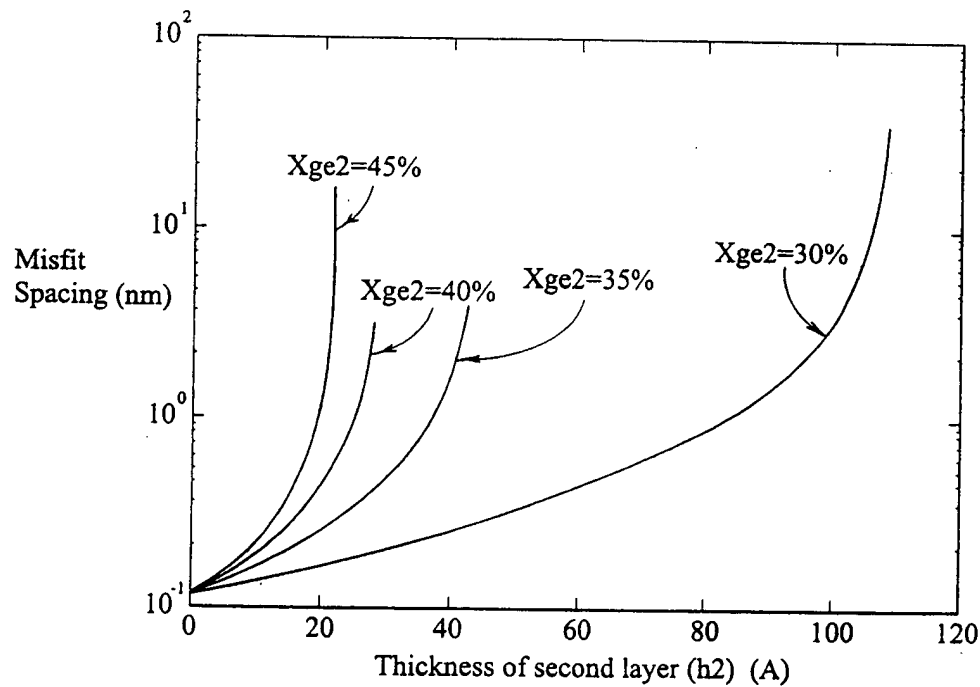
*FIG. 8*

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12/12



**FIG. 9**



**FIG. 10**